

How to do

Determine firmwareversion from Speed7-CPU

1. Read internal webpage with MMC (min. Firmware V2.00 needed)
MMC with "Commandfile": vipa_cmd.mmc

Edit/enter the content of vipa_cmd.mmc with a text editor:

```
CMD_START
WEBPAGE
CMD_END
```

STOP the CPU

Plug the MMC-Card with the file „vipa_cmd.mmc“ into CPU, wait some seconds till MMC-LED is flashing one time, unplug MMC-Card and read it with a PC. On MMC there is now a file „webpage.htm“ which you can open with a standard webbrowser.
(see Screenshot)

2. Read internal webpage directly from CPU with webbrowser. A minimum project is needed on the CPU.

HW-Config:

Slot 2: CPU 318, V3.0

Slot 4: CP343-1EX11 (internal Ethernet CP for PG/OP) IP-Adress, Sub-Net-Mask, Networked

After download this HW-Config into the CPU, you can connect to the webserver of CPU by typing the IP-Adress into a standard webbrowser.
(see Screenshot)

Speed7

CPU with Ethernet-PG/OP
Slot 100
VIP A 317-4NE11 V3.1.0 P:000053.pkg, SERIALNUMBER 04675
SUPPORTDATA : PRODUCT V3100, HARDWARE V0220, 5679A-V20, B:000226 V6100, A:000055 V1090, A:000056 V0200, FlashFileSystem : V102
OnBoardEthernet : MacAddress : 0020D5771243, IP-Address : 172.16.129.221, SubnetMask : 255.255.224.0, Gateway : 172.16.129.221
Cpu state : Stop
FunctionRS485 : DPM-async
Cycletime [microseconds] : min=0 cur=0 ave=0 max=0

Slot 201
VIP A 342-1DA70 V3.0.4 P:000009.pkg, SUPPORTDATA : PRODUCT V3043, BB000181 V5043, AB000068 V4100, AB000069 V4100,
ModuleType CB2C0010
Cycletime [microseconds] : min=65535000 cur=0 ave=0 max=0 cnt=0

Slot 206
VIP A 343-1EX71 V1.9.5 P:000058.pkg, SUPPORTDATA : B:000165 V1950, AB000060 V0320, PRODUCT V1950,
ModuleType ACDB0000
Address Input 1024...1039
Address Output 1024...1039

Speed Bus
Slot 103
VIP A 343-1EX71 V1.9.4 P:000005.pkg, SUPPORTDATA : B:000165 V1940, AB000060 V0320, AB000061 V0310, PRODUCT V1940,
ModuleType ACDB0001
Address Input 2560...2575
Address Output 2560...2575

Standard Bus 8 Bit Mode

For more information about speed7 technology and products
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